

**M.D.UNIVERSITY, ROHTAK**  
**SCHEME OF STUDIES AND EXAMINATION**  
**M.TECH 1st YEAR (ELECTRONICS & COMMUNICATION)**  
**SEMESTER I**  
**CBCS Scheme effective from 2016-17**

Sl. No	Course Code	Subject	Credit Pattern			Examination Schedule (Marks)			Dura- tion of Exam (Hou- rs)	No of Hours /week	
			L	T	P	Total Credi- ts	Mark s of Class work	Theor- y	Practic- al		
1	MTECE21C1	Advance Microprocessor & Microcontroller	4	0	-	4	50	100	-	150	3
2	MTECE21C2	Satellite and Space Communication	4	0	-	4	50	100	-	150	3
3	MTECE21C3	Information and Communication Theory	4	0	-	4	50	100	-	150	3
4	MTECE21C4	Advanced Digital Signal Processing	4	0	-	4	50	100	-	150	3
5	MTECE21C5	Data Communication Networks	4	0	-	4	50	100	-	150	3
6	MTECE21C6	Seminar	-	-	-	2	50	-	-	50	2
7	MTECE21C7	Satellite Lab	-	-	2	2	50	-	50	100	3
8	MTECE21C8	Advance Microprocessor & Microcontroller Lab	-	-	2	2	50	-	50	100	3
<b>TOTAL</b>			26								

NOTE:

Examiner will set nine question in total. Question One will be compulsory and will comprise short answer type questions from all sections and remaining eight questions to be set by taking two questions from each unit. The students have to attempt five questions in total, first being compulsory and selecting one from each Unit.

**M.D.UNIVERSITY, ROHTAK**  
**SCHEME OF STUDIES AND EXAMINATION**  
**M.TECH 1st YEAR (ELECTRONICS & COMMUNICATION)**  
**SEMESTER 2**  
**CBCS Scheme effective from 2016-17**

Sl N o	Course No.	Subject	Credit Pattern			Examination Schedule (Marks)			Duration of Exam (Hours)	No of Hours/ week		
			L	T	P	Total Credi ts	Marks of Class works	Theory	Practical			
1	MTECE22C1	Wireless Mobile Communication	4	0	-	4	50	100	-	150	3	4
2	MTECE22C2	Optical Communication	4	0	-	4	50	100	-	150	3	4
3	MTECE22C3	Seminar	-	-	-	2	50	-	-	50	-	2
4	MTECE22C4	VLSI Lab	-	-	2	2	50	-	50	100	3	4
5	MTECE22C5	Optical Communication Lab	-	-	2	2	50	-	50	100	3	4
6	MTECE22D1 or MTECE22D2 or MTECE22D3 or MTECE22D4	Elective-I	4	0	-	4	50	100	-	150	3	4
7		Open Elective				3						
8		Foundation Elective				2						
		<b>TOTAL</b>				23						

**NOTE:** Examiner will set nine questions in total. Question One will be compulsory and will comprise short answer type questions from all sections and remaining eight questions to be set by taking two questions from each unit. The students have to attempt five questions in total, first being compulsory and selecting one from each Unit.

**Elective I :** Choose any one from the following four papers:

- MTECE22D1 - Electronic System Design
- MTECE22D2 - Image Processing
- MTECE22D3 - ADVANCED MATHEMATICS FOR ENGINEERS
- MTECE22D4 - VLSI Design

**Open Elective:** A candidate has to select this paper from the pool of Open Electives provided by the University.

**Foundation Elective:** A candidate has to select this paper from the pool of Foundation Electives provided by the University.