

NPTEL LINKS

Sem :- VI (ECE)

Subject :- DIGITAL SYSTEM DESIGN

Section		NPTEL Links
A	Introduction to VHDL, Data object, data type, Operator, Delay, overloading	http://nptel.ac.in/courses/117108040/9 http://nptel.ac.in/courses/117108040/13 http://nptel.ac.in/courses/117108040/19 https://www.youtube.com/watch?v=mAkpPShuYI
B	Concurrent assignment and sequential assignment, library and package	http://nptel.ac.in/courses/117108040/14 http://nptel.ac.in/courses/117108040/18
C	Behavioral, dataflow modeling, modeling flip flops and registers, Modeling styles	http://nptel.ac.in/courses/117108040/16 https://www.youtube.com/watch?v=4oyNtuxh4D0 http://nptel.ac.in/courses/117108040/10
D	PLDs, FPGA,CPLD	http://nptel.ac.in/courses/117108040/35 http://nptel.ac.in/courses/117106086/31 http://nptel.ac.in/courses/117108040/34