

NPTEL LINKS

Subject :- VLSI Design (EE-306-F)

Section	NPTEL Links	NPTEL Links
A	<ol style="list-style-type: none">1. Introduction to VLSI Design2. MOSFET and Fabrication Process	<ol style="list-style-type: none">1. http://nptel.ac.in/courses/117106092/12. http://nptel.ac.in/courses/Webcourse-contents/IIT-Bombay/VLSI%20Design/TOC.htm
B	<ol style="list-style-type: none">1. Layout Design & Propagation Delay	<ol style="list-style-type: none">1. http://nptel.ac.in/courses/Webcourse-contents/IIT-Bombay/VLSI%20Design/TOC.htm
C	<ol style="list-style-type: none">1. Combinational Circuit Design2. Sequential Circuit Design	<ol style="list-style-type: none">1. http://nptel.ac.in/courses/117106092/22. http://nptel.ac.in/courses/117106092/7
D	<ol style="list-style-type: none">1. Programmable Logic Design2. Programmable Array Logic3. RTL Coding4. Design Flow of VLSI Circuits5. System Design Examples using FPGA Board	<ol style="list-style-type: none">1. http://nptel.ac.in/courses/117106092/32. http://nptel.ac.in/courses/117106092/43. http://nptel.ac.in/courses/117106092/134. http://nptel.ac.in/courses/117106092/235. http://nptel.ac.in/courses/117106092/48