PIC Addressing Modes

Addressing Modes

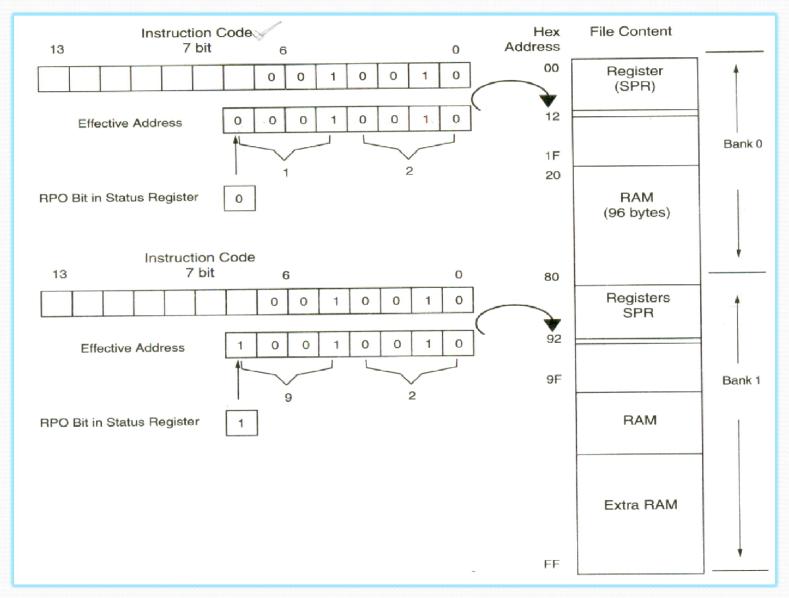
Direct Addressing

It uses 7 bits of instruction and the 8th bit from RP0.If bit is 0 then bank 0 otherwise bank 1.

Indirect addressing

In this mode the 8 bit address of the location in register file to be accessed is written in FSR and use INDF.

DIRECT ADDRESSING



INDIRECT ADDRESSING

