



Workshop on Cognitive  
Radio using USRP at  
ISM Dhanbad

• 6th -10th January 2015

IEEE Robotics Workshop  
at Dronacharya College  
of Engineering by IEEE  
DTU Student Branch

• 28th January 2015

Workshop on "VIRTUAL  
LABS"

• 8th April 2015

**Workshop on Cognitive Radio using USRP at ISM Dhanbad**  
**from 6-10 Jan 15.**

**Mr. Ashish Gambhir** Assistant Professor from Electronics and Communication Engineering Department attended a workshop on “*Cognitive Radio using USRP*” at **Indian School of Mines, Dhanbad** from **6-10 Jan 15** organized by Department of Electronics Engineering of the Institution. The workshop was very beneficial to all the participants as they gained knowledge on the latest developments in this subject.



**Participants solving assignment**



## IEEE Robotics Workshop at Dronacharya College of Engineering by IEEE DTU Student Branch on 28 Jan 15

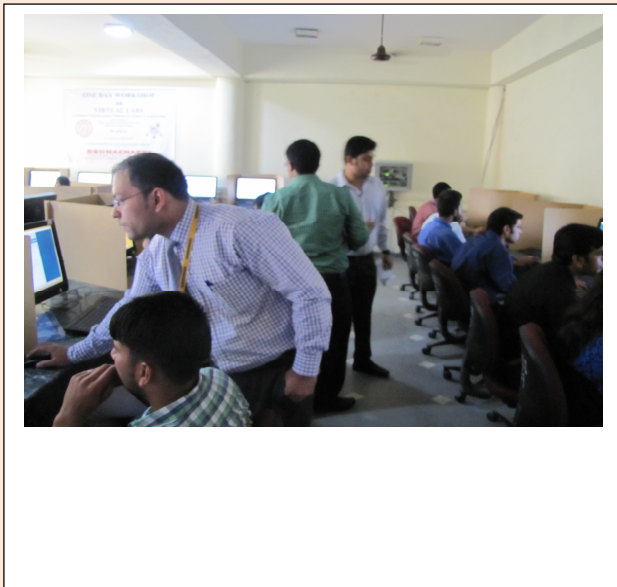
IEEE DCEG Student Branch organized Robotics Workshop at Dronacharya College of Engineering sponsored by IEEE DTU Student Branch on 28 Jan 15. This workshop aimed to smoothen their journey in the field of robotics and to promote robotics right from the grass roots level.



## Workshop on “VIRTUAL LABS” on 8 Apr 15 in association with IIT Delhi.

Dronacharya College of Engineering, Gurgaon, being Nodal Center of Virtual Labs of IIT Delhi, conducted one day workshop on “**Virtual Labs**” on 8 Apr 15 in association with IIT Delhi.

Virtual Labs is an initiative of Ministry of Human Resource Development (**MHRD**), Govt of India, under the National mission on Education through ICT (**NMEICT**). A total of 73 students of Electronics, Electrical, Computer, IT and ME streams participated in the workshop including students and faculty from outside colleges





Development of Virtual Lab experiments for V LAB portal in association with iit delhi

**The following labs have been developed and submitted to IIT Delhi for gap analysis (if any) , and uploading on V Lab portal:**

- **Control System Engineering Lab**
- **Microprocessor Lab**
- **Compiler Design Lab**
- **C++ Lab**