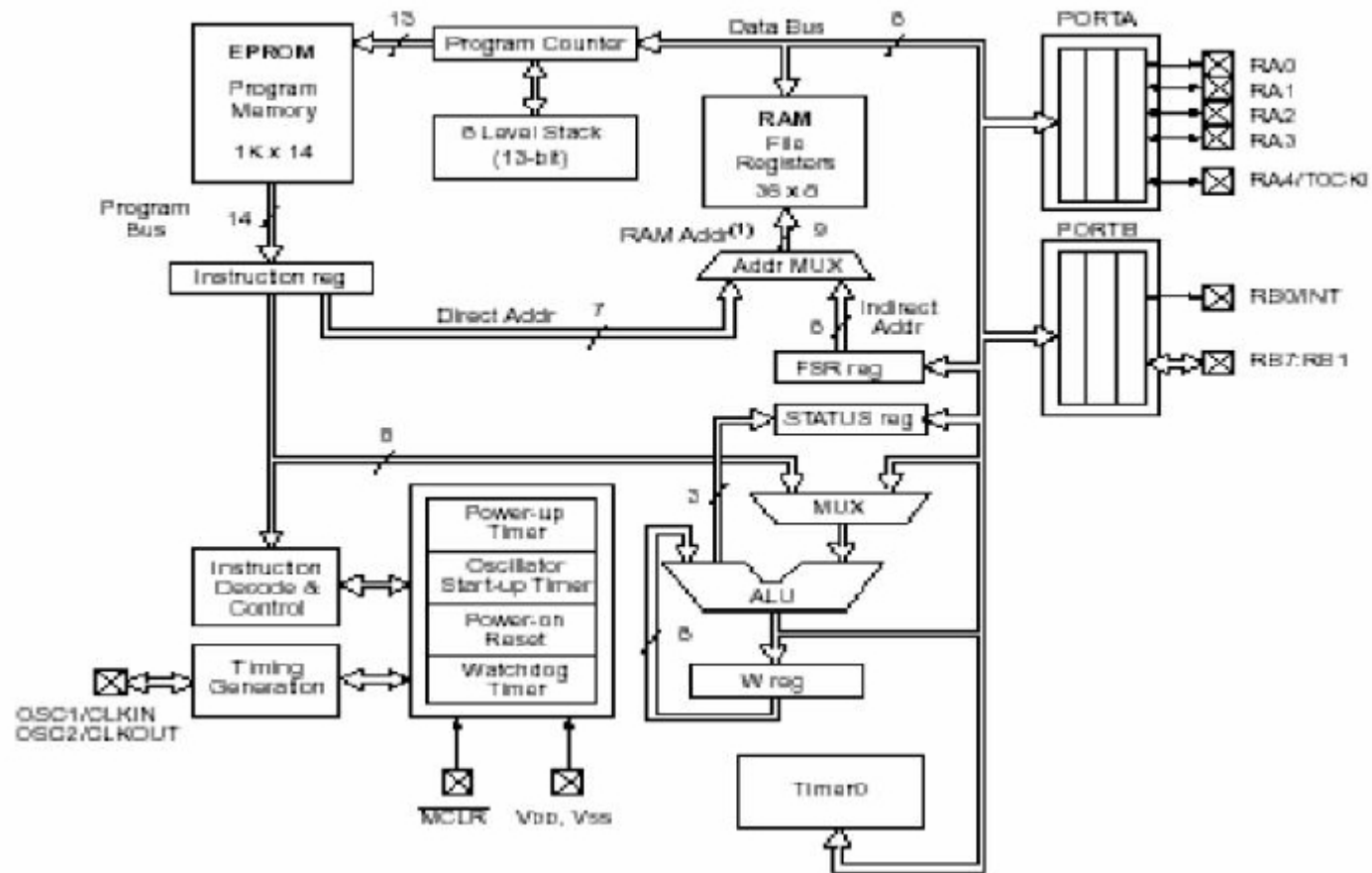# LECTURE 24

Architecture

Topics to be covered

- Instructions

PIC Internal Architecture

PIC Architecture

- **ALU**

  Size is 8 bit

  Performs operations with temporary working register and (W register) and any register file.

- **W register**

  8 bit wide. It contains one of source operands during execution of instruction and may serve as the destination for the result of operation.

  Used only for ALU operations.

Architecture Continued...

- **STATUS Register**

7	6	5	4	3	2	1	0
0	0	RP0	T0	PD	Z	DC	C

C = Carry Bit

DC = Digits Carry (Same as AC)