# **Analog Communication Systems EC-413-F**

## Lecture no 3

# Topics to be covered

**Pulse Modulation** 

#### Sampling Theorem and its Importance

• Sampling Theorem:

"A bandlimited signal can be reconstructed exactly if it is sampled at a rate atleast twice the maximum frequency component in it."

Figure 1 shows a signal g(t) that is bandlimited.

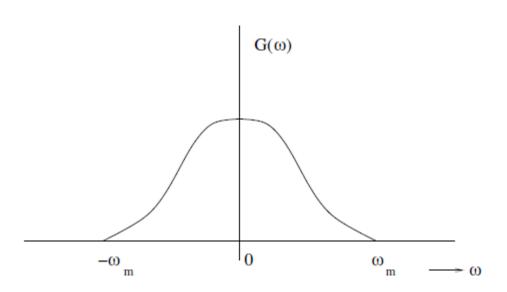


Figure 1: Spectrum of bandlimited signal g(t)

- The maximum frequency component of g(t) is  $f_m$ . To recover the signal g(t) exactly from its samples it has to be sampled at a rate  $f_s \geq 2f_m$ .
- The minimum required sampling rate  $f_s = 2f_m$  is called

Nyquist rate.

Proof: Let g(t) be a bandlimited signal whose bandwidth is  $f_m$   $(\omega_m = 2\pi f_m)$ .

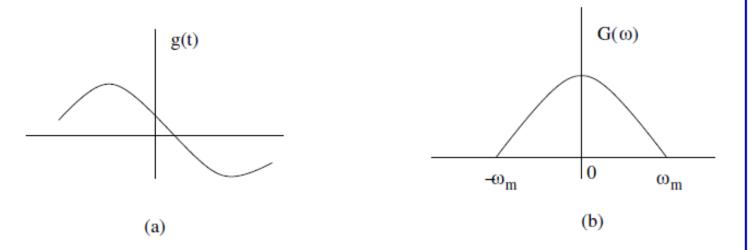


Figure 2: (a) Original signal g(t) (b) Spectrum  $G(\omega)$ 

 $\delta_T(t)$  is the sampling signal with  $f_s = 1/T > 2f_m$ .

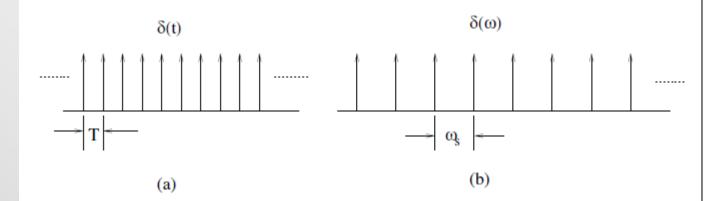


Figure 3: (a) sampling signal  $\delta_T(t)$  (b) Spectrum  $\delta_T(\omega)$ 

• Let  $g_s(t)$  be the sampled signal. Its Fourier Transform  $G_s(\omega)$  is given by

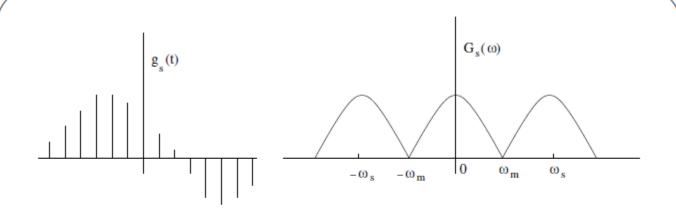
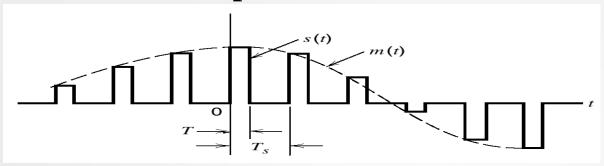


Figure 4: (a) sampled signal  $g_s(t)$  (b) Spectrum  $G_s(\omega)$ 

• If  $\omega_s = 2\omega_m$ , i.e.,  $T = 1/2f_m$ . Therefore,  $G_s(\omega)$  is given by

$$G_s(\omega) = \frac{1}{T} \sum_{n=-\infty}^{+\infty} G(\omega - n\omega_m)$$

#### 3.3 Pulse-Amplitude Modulation



Let s(t) denote the sequence of flat - top pulses as

$$s(t) = \sum_{n = -\infty}^{\infty} m(nT_s) h(t - nT_s)$$
(3.10)

$$h(t) = \begin{cases} 1, & 0 < t < T \\ \frac{1}{2}, & t = 0, t = T \\ 0, & \text{otherwise} \end{cases}$$
 (3.11)

The instantaneously sampled version of m(t) is

$$m_{\delta}(t) = \sum_{n=-\infty}^{\infty} m(nT_s)\delta(t - nT_s)$$
 (3.12)

$$m_{\delta}(t) * h(t) = \int_{-\infty}^{\infty} m_{\delta}(\tau)h(t-\tau)d\tau$$

$$= \int_{-\infty}^{\infty} \sum_{n=-\infty}^{\infty} m(nT_s)\delta(\tau - nT_s)h(t-\tau)d\tau$$

$$= \sum_{n=-\infty}^{\infty} m(nT_s) \int_{-\infty}^{\infty} \delta(\tau - nT_s)h(t-\tau)d\tau \quad (3.13)$$

Using the sifting property, we have

$$m_{\mathcal{S}}(t) * h(t) = \sum_{s}^{\infty} m(nT_s)h(t - nT_s)$$
 (3.14)

## The PAM signal s(t) is

$$s(t) = m_{\delta}(t) * h(t) \tag{3.15}$$

$$\Leftrightarrow S(f) = M_{\delta}(f)H(f) \tag{3.16}$$

Recall (3.2) 
$$g_{\delta}(t) \Leftrightarrow f_{s} \sum_{m=-\infty}^{\infty} G(f - mf_{s})$$
 (3.2)

$$\mathbf{M}_{\delta}(f) = f_{s} \sum_{k=-\infty}^{\infty} M(f - k f_{s})$$
 (3.17)

$$S(f) = f_s \sum_{k=-\infty}^{\infty} M(f - kf_s)H(f)$$
 (3.18)

# Pulse Amplitude Modulation – Natural and Flat-Top Sampling

- The circuit of Figure 11-3 is used to illustrate pulse amplitude modulation (PAM). The FET is the switch used as a sampling gate.
- When the FET is on, the analog voltage is shorted to ground; when off, the FET is essentially open, so that the analog signal sample appears at the output.
- Op-amp 1 is a non inverting amplifier that isolates the analog input channel from the switching function.

# Pulse Amplitude Modulation – Natural and Flat-Top Sampling

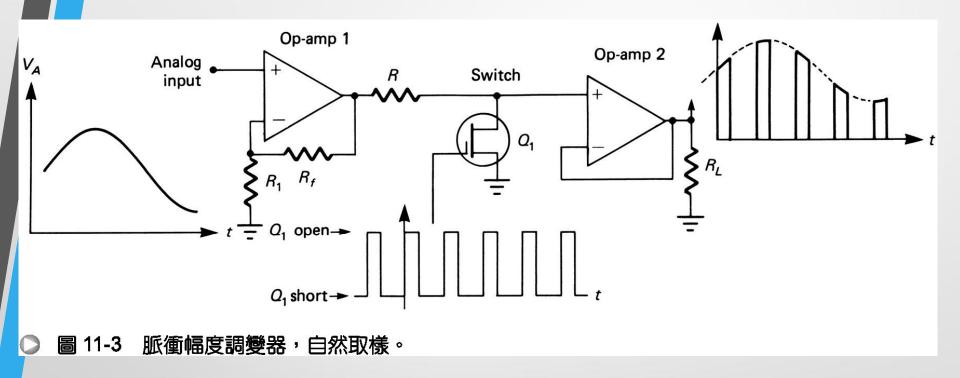


Figure 11-3. Pulse amplitude modulator, natural sampling.

# Pulse Amplitude Modulation – Vatural and Flat-Top Sampling

- Op-amp 2 is a high input-impedance voltage follower capable of driving low-impedance loads (high "fanout").
- The resistor R is used to limit the output current of op-amp 1 when the FET is "on" and provides a voltage division with  $r_{\rm d}$  of the FET. ( $r_{\rm d}$ , the drain-to-source resistance, is low but not zero)

# Pulse Amplitude Modulation – Vatural and Flat-Top Sampling

- The most common technique for sampling voice in PCM systems is to use a sample-and-hold circuit.
- As seen in Figure 11-4, the instantaneous amplitude of the analog (voice) signal is held as a constant charge on a capacitor for the duration of the sampling period T<sub>s</sub>.
- This technique is useful for holding the sample constant while other processing is taking place, but it alters the frequency spectrum and introduces an error, called aperture error, resulting in an inability to recover exactly the original analog signal.

# Pulse Amplitude Modulation – Natural and Flat-Top Sampling

- The amount of error depends on how much the analog changes during the holding time, called aperture time.
- To estimate the maximum voltage error possible, determine the maximum slope of the analog signal and multiply it by the aperture time ΔT in Figure 11-4.

# Pulse Amplitude Modulation – Natural and Flat-Top Sampling

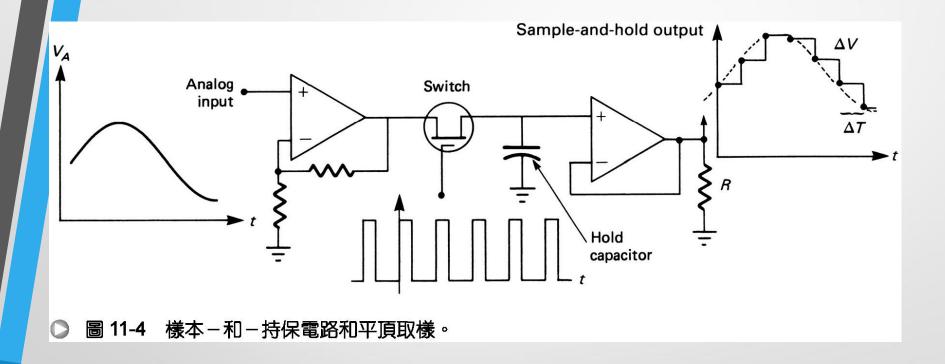
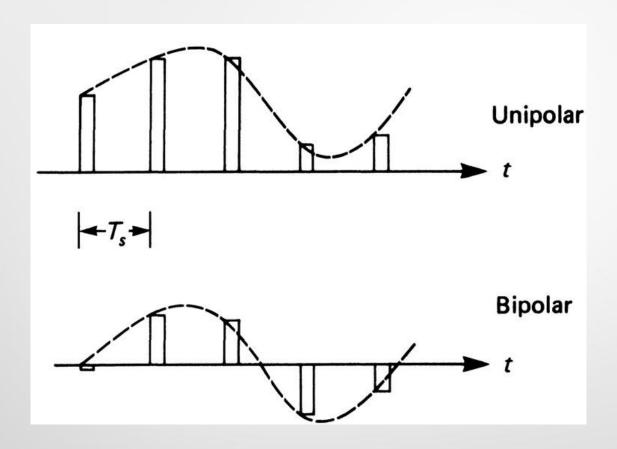


Figure 11-4. Sample-and-hold circuit and flat-top sampling.

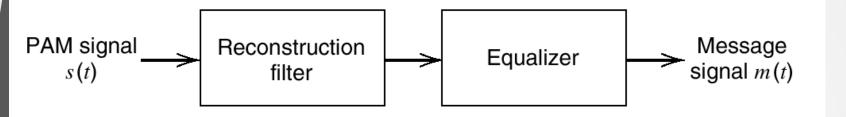
# Pulse Amplitude Modulation — Natural and Flat-Top Sampling



■ 11-5 平頂 PAM 訊號。

Figure 11-5. Flat-top PAM signals.

#### **Rec**overing the original message signal m(t) from PAM signal



Where the filter bandwidth is W

The filter output is  $f_sM(f)H(f)$ . Note that the

Fourier transform of h(t) is given by

$$H(f) = T \operatorname{sinc}(fT) \exp(-j\pi fT)$$
amplitude distortion delay =  $\frac{T}{2}$  (3.19)

 $\Rightarrow$  aparture effect

Let the equalizer response is

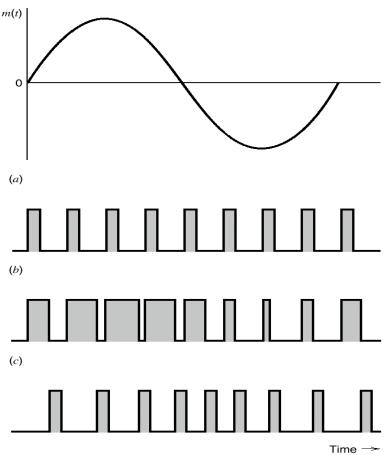
$$\frac{1}{H(f)} = \frac{1}{T\operatorname{sinc}(fT)} = \frac{\pi f}{\sin(\pi fT)}$$
(3.20)

Ideally the original signal m(t) can be recovered completely.

#### 3.4 Other Forms of Pulse Modulation

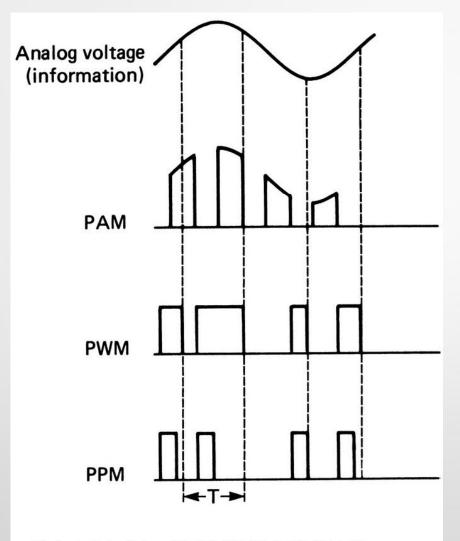
- a. Pulse-duration modulation (PDM)
- b. Pulse-position modulation (PPM)

PPM has a similar noise performance as FM.



(d)

- In pulse width modulation (PWM), the width of each pulse is made directly proportional to the amplitude of the information signal.
- In pulse position modulation, constant-width pulses are used, and the position or time of occurrence of each pulse from some reference time is made directly proportional to the amplitude of the information signal.
- PWM and PPM are compared and contrasted to PAM in Figure 11-11.



○圖 11-11 類比脈衝調變訊號。

Figure 11-11. Analog/pulse modulation signals.

- Figure 11-12 shows a PWM modulator. This circuit is simply a high-gain comparator that is switched on and off by the sawtooth waveform derived from a very stable-frequency oscillator.
- Notice that the output will go to +V<sub>cc</sub> the instant the analog signal exceeds the sawtooth voltage.
- The output will go to -V<sub>cc</sub> the instant the analog signal is less than the sawtooth voltage. With this circuit the average value of both inputs should be nearly the same.
- This is easily achieved with equal value resistors to ground. Also the +V and –V values should not exceed V<sub>cc</sub>.

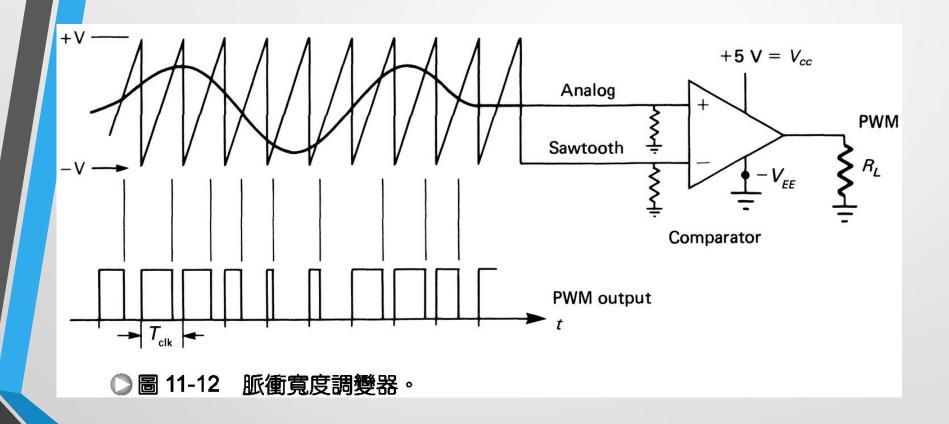


Figure 11-12. Pulse width modulator.

- A 710-type IC comparator can be used for positive-only output pulses that are also TTL compatible. PWM can also be produced by modulation of various voltagecontrollable multivibrators.
- One example is the popular 555 timer IC. Other (pulse output) VCOs, like the 566 and that of the 565 phaselocked loop IC, will produce PWM.
- This points out the similarity of PWM to continuous analog FM. Indeed, PWM has the advantages of FM--constant amplitude and good noise immunity---and also its disadvantage---large bandwidth.

- Since the width of each pulse in the PWM signal shown in Figure 11-13 is directly proportional to the amplitude of the modulating voltage.
- The signal can be differentiated as shown in Figure 11-13 (to PPM in part a), then the positive pulses are used to start a ramp, and the negative clock pulses stop and reset the ramp.
- This produces frequency-to-amplitude conversion (or equivalently, pulse width-to-amplitude conversion).
- The variable-amplitude ramp pulses are then time-averaged (integrated) to recover the analog signal.

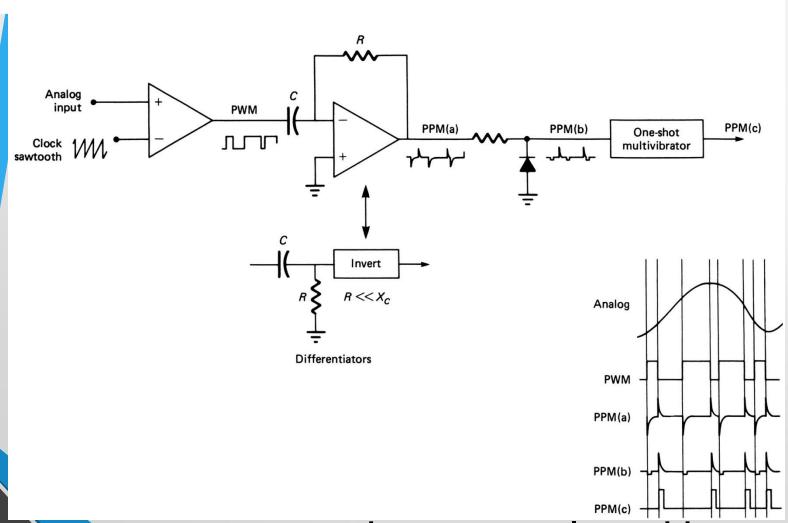


Figure 11-13. Pulse position modulator.

- As illustrated in Figure 11-14, a narrow clock pulse sets an RS flip-flop output high, and the next PPM pulses resets the output to zero.
- The resulting signal, PWM, has an average voltage proportional to the time difference between the PPM pulses and the reference clock pulses.
- Time-averaging (integration) of the output produces the analog variations.
- PPM has the same disadvantage as continuous analog phase modulation: a coherent clock reference signal is necessary for demodulation.
- The reference pulses can be transmitted along with the PPM signal.

- This is achieved by full-wave rectifying the PPM pulses of Figure 11-13a, which has the effect of reversing the polarity of the negative (clock-rate) pulses.
- Then an edge-triggered flipflop (J-K or D-type) can be used to accomplish the same function as the RS flipflop of Figure 11-14, using the clock input.
- The penalty is: more pulses/second will require greater bandwidth, and the pulse width limit the pulse deviations for a given pulse period.

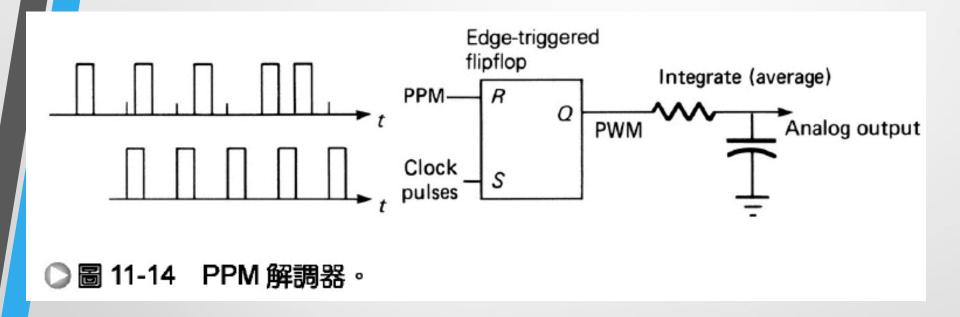


Figure 11-14. PPM demodulator.