Dronacharya College of Engineering, Gurgaon

Department of Electronics and Computers Engineering

Subject: Computer Architecture & Organization (CSE-210-F)

Semester: IV/ Branch: ECS

Short Answer questions

Section A

- 1. How is MIPS used as a performance metric?
- 2. What is cache memory?
- 3. What do MISD and MIMD stand for?
- 4. What is a multiplexer? Give the diagram of 2:4 mux.
- 5. What are sequential logic blocks?
- 6. Give the memory hierarchy.

Section B

- 1. What is the difference between RISC and CISC?
- 2. Give examples of arithmetic & logical operations.
- 3. What is direct addressing mode? Give example.
- 4. What is instruction set format?
- 5. What is hybrid instruction set?

Section C

- 1. What are the different types of cache organization?
- 2. What is access/cycle time memory parameter?
- 3. What is microinstruction sequencing?
- 4. What is dynamic memory?
- 5. What is the function of cache memory?
- 6. What is pipelining?

Section D

- 1. What is Amdahl's law?
- 2. What is parallelism?
- 3. What are the goals of parallelism?
- 4. What are the different types of instructions?
- 5. What does address sequencing mean?
- 6. Name the different types of interrupts.
- 7. What is I/O reference?