Centralized Campus Placement for Data Patterns

DCE/TPO/ 4710 24 Dec 2007

There is an opportunity for campus placement with Data Patterns. Desirous students are required to contact to Training and Placement Division latest by **26 Dec 2007**, so that their names can be forwarded to the company. Other details are as follows:

Company : Data Patterns

Position :

• Trainee Software Engineer

Trainee Hardware Design Engineer

Eligibility Criteria: More than 60% in B.E.

Eligible Branches : ECE, CSE and IT

Batch : 2008

Venue : Sukhmani Devi Institute of Technology

Derabassi -140507, (Near Chandigarh)

Date of Interview: Tentative fourth week of Jan 2008

Salary Package : During Training Period - Rs. 10,000 per month + Medical and Accident Policy

Position 01: Software Engineer (Trainee)

Job Profile:

- Software Requirements study and architecture design
- Coding in C, C++, VC++ for CISC/RISC processors
- Device driver development, OS porting etc.
- Testing the software
- Debugging the software & Installation at the DRDO/SPACE labs

No. of vacancies: 100

Subjects for technical test: C, C++, Data Structures, OS Concepts, CISC/RISC Processor + Aptitude Test

Position 02: Hardware Design Engineer (Trainee)

- Hardware Requirements study & Data Sheet preparation
- Schematic design (Analog and Digital circuit design) using Orcad ver.9.0
- FPGA design
- Preparing Bill of Material (BOM) for the design, Component selection
- Assisting PCB design Engineer in Routing the signals
- Releasing the register details for software coding
- Testing the pcb's & Hardware trouble shooting

No. of vacancies: More than 100

Subjects for technical test: Basics of Analog & Digital Electronics + Aptitude Test

Date of Joining : 16th June 2008 (Monday)

Note

- Students are informed to carry all the mandatory documents like hard copy of resume, passport size photographs, photocopies of all semester DMC's and proof of identity.
 Students are advised to remain in touch with the undersigned.